Cyber-Engineer

"The Low Noise Amplifier - LNA" (outline)

Noise Figure of a Two-Port Device
Practical Noise Figure Measurement (Y-Factor)
Noise Parameter Expressions
Input noise and power match
Matching Network Design Compromising Gain and Noise
Gain Circles - Associated gain - Noise circles
LNA Design Limitations
LNA Topologies Compared
LNA Design Flowchart & Methods
Biasing and device sizing for gain and noise
Design using Simultaneous Noise and Impedance Match (SNIM)
Design Examples using Power Constrained Noise Optimization (PCNO)
Load matching network design

Contact your Account MRE at **Theta-j Associates**
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This is a free service that strengthens our relationship with our customers.

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